

**AMENDMENTS TO THE CLAIMS**

1.-21. (canceled)

22. (currently amended) A method for mounting ~~and operating~~ multilayered ceramic capacitors on a circuit board ~~having a front surface and a back surface~~, wherein each capacitor includes a body having dielectric layers formed of a dielectric ceramic material and internal electrode layers and a pair of external terminal electrodes formed on two sides of the body, the dielectric layers and the internal electrode layers being stacked alternately in the body and the internal electrode layers being connected in parallel to the external terminal electrodes in an alternate manner, the method comprising the steps of:

(a) forming lands preparing the circuit board having a front and a back surface on which lands for mounting the capacitors thereon are formed at substantially plane-symmetrical positions ~~on the front and the back surfaces~~, wherein every two lands disposed at their substantially plane-symmetrical positions are connected to each other;

(b) mounting the capacitors on the lands of the front and the back surface to cancel out vibrations generated from the capacitors; surfaces and

(c) electrically coupling the external terminal electrodes to the land of the capacitors to the lands on the front and the back surface; and

applying to the capacitors voltages which have frequencies varying in an audible frequency band, respectively.

23.-40. (canceled)

41. (new) The method of claim 22, wherein a length, a width and a height of one of the capacitors range from about 70 to about 130% of those of the other capacitor, respectively, to cancel out the vibrations generated from the capacitors.

42. (new) The method of claim 22, wherein offsets between the capacitors are less than about 30% of a length and a width of one of the capacitors along the directions of the length and the width, respectively, to cancel out the vibrations generated from the capacitors.

43. (new) The method of claim 22, wherein an offset between center axes along length directions of the capacitors is less than 40 degrees to cancel out the vibrations generated from the capacitors.

44. (new) The method of claim 22, wherein said every two lands are electrically coupled to each other by a through hole formed therein.

45. (new) The method of claim 22, wherein substantially identical voltages are applied to the capacitors.

46. (new) The method of claim 22, wherein the capacitors are used as a component of an electronic circuit in which voltages applied to the capacitors are varied.

47. (new) The method of claim 22, wherein the capacitors are connected in parallel.

48. (new) The method of claim 22, wherein voltages applied to the capacitors are varied.

49. (new) The method of claim 22, wherein voltages applied to the capacitors have frequencies varying in an audible frequency band.